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	9. CONTACT HOURS PER WEEK: 3 LECTURE LAB PRACTICUM hours/week hours /week hours /week	
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	of lab in a science course=1 credit. 1600 minutes in non-science lab=1 credit. 2400-4800 minutes of practicum=1 credit. 2400-8000 minutes of internship=1 credit. This must match with the sullabus See http://www.usf.edu/usfoow/faculty-sopato/curriculum/course-dogree-precedures	
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	/guidelines-for-computing-/ for more information on number of credits.	
	OTHER HOURS (specify type)	
	10. COMPLETE CATALOG DESCRIPTION including dept., number, title, credits, credit distribution, cross-listings and/or stacking (50 words or less if possible):	
	Example of a <u>complete</u> description: FISH F487 W, O Fisheries Management 3 Credits Offered Spring Theory and practice of fisheries management, with an emphasis on strategies utilized for the management of freshwater and marine fisheries. Prerequisites: COMM F131X or COMM F141X; ENGL F111X; ENGL F211X or ENGL F213X; ENGL F414; FISH F425; or permission of instructor. Cross-listed with NRM F487. (3+0)	
	EE F648 VLSI Design	
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	RESTRICTIONS ON ENRO		_
	14. PREREQUISITES	EE 343 or equivalent	
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APPROVALS: Add additional signature lines as needed. 8/23/13 Signature, Chair, Computer Engineering Electrical and

EE 693 VLSI Design

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Instructor:

Dr. Jason McNeely, Duckering 227

Office Phonos 474 7728

Email: jbmcneely@alaska.edu

Office Hours: Monday 2:00-4:00; Thursday 2:30-4:30

(You may also schedule an appointment or just drop by. The best way to

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Lectures:

MWF 10:30-11:30

Location:

DUCK 406

Credits:

3

Textbook:

CMOS VLSI Design: A Circuits and Systems Perspective 4th Edition, Neil Weste and

David Harris. 2011 Pearson Education. ISBN 978-0-321-54774-3

References:

Other reference materials may be posted electronically during this course

COURSE GOALS/OUTCOMES

Students taking this course will be able to:

- Design CMOS circuits at the transistor level and use CAD tools to create the chip
- Start with a design concept at a transistor level or schematic and produce a layout
- Understand the industry standard methods of logic design and layout

Student learning outcomes would be the ability to:

- Design full custom IC VLSI design using the MAGIC CAD tool
- Design logic circuits using CMOS transistor family
- Comprehend performance issues in CMOS circuits such as power and delay
- Have an ability to create dynamic CMOS designs
- Cincillate and analysis the nonformance of levels

Thursday January 17:	First day of classes Alacka Civil Rights Day (no alacces)
Friday February 1:	Last day to drop the class (course does not appear on academic record)
Finday reordary 1.	Last day to drop the class (course does not appear on deaderme record)
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Friday March 22:

Last day to withdraw ('W' appears on academic record)

Friday April 26: Monday May 6:

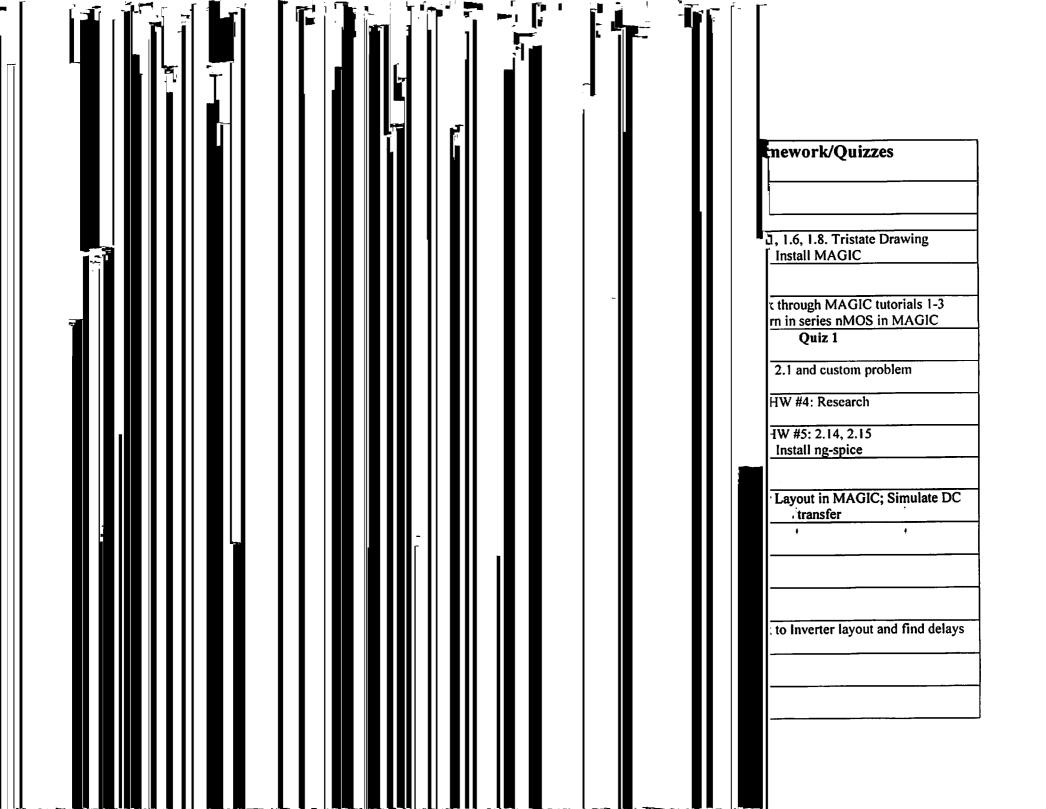
UAF SpringFest (no classes) Last day of class

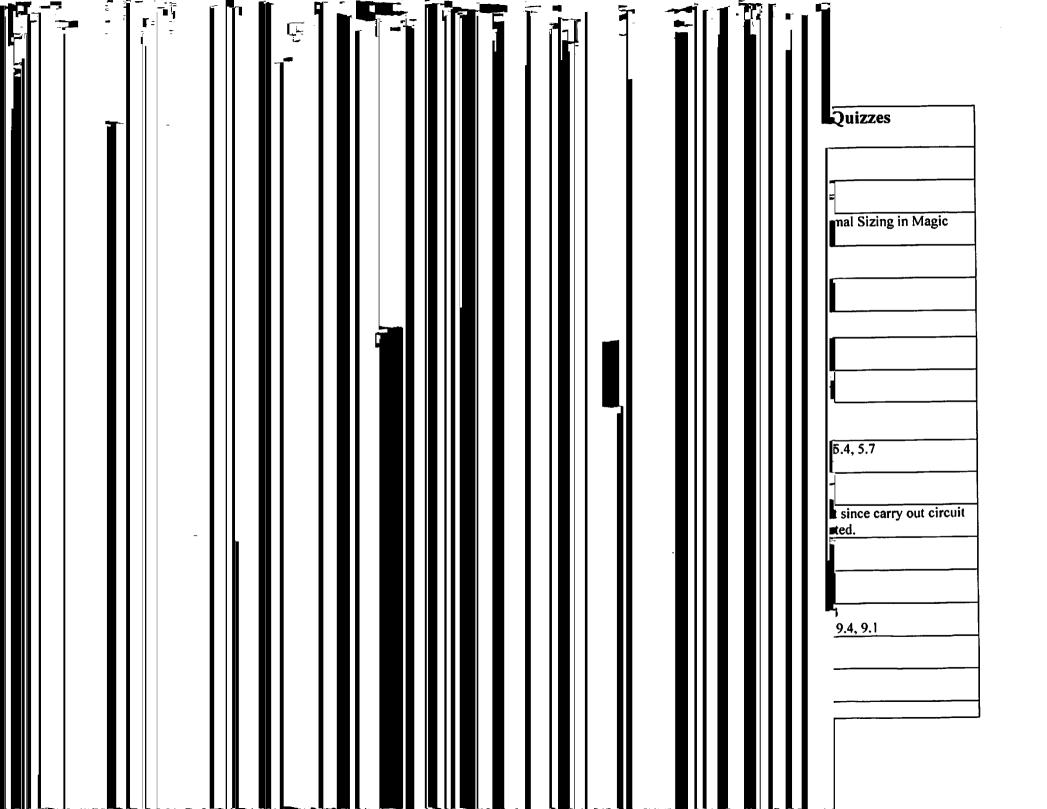
Tuesday-Friday May 7-10:

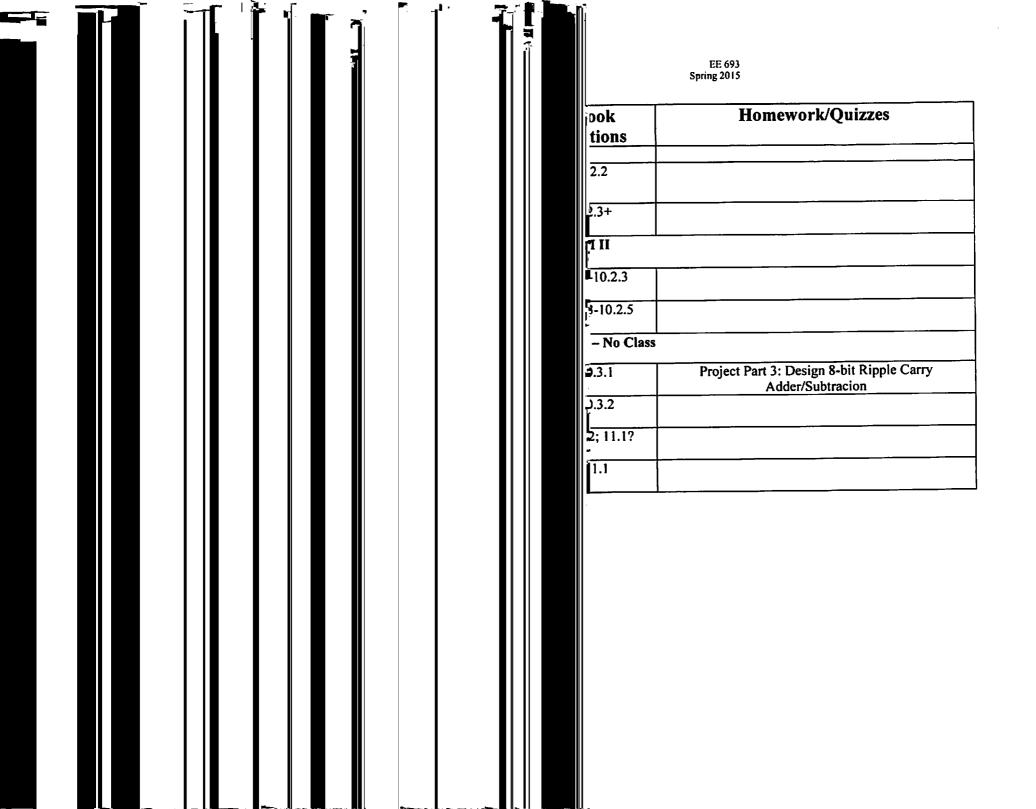
FINAL EXAMS

May 8 10:15-12:15

FINAL EXAM for VLSI







TENTATIVE SCHEDULE 42 Class Meetings = 37 Lectures + 1 Tutorial Day + 2 Exams + 2 Presentation days FINAL EXAM: May 8 10:15-12:15 COURSE POLICIES E-mail Each student is expected to regularly check his or her alaska.edu email address. This address will be used for class correspondence - announcements, homework problems clarifications, etc. If you are not using the official

Attendance

Class attendance is highly recommended. Material not in the text may be introduced at random intervals and occasional quizzes are part of your grade. If you miss a class, lecture slides and other handouts are available on

Blackboard or can be obtained from the instructor. If you are late, please enter without disrupting the class.

Homework

Homework problems will typically be due the following class, unless otherwise stated. No late homework will

Course Syllabus EE 693 Spring 2015	
Students with learning or other disabilities who may need classroom accommodations are encouraged to make an appointment with the Office of Disability Services (208 WHIT, Phone # 474-5655). Please meet with me during office hours so that we can collaborate with the Office of Disability Services to provide the appropriate accommodations and supports to assist you in meeting the goals of the course.	
Homework 15%	

Plus/Minus grading will be used.

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